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PATENT NUMBER and  
ISSUE DATE

U.S. UTILITY Patent Application

APPL NUM 10083254	FILING DATE 02/27/2002	CLASS 716	SUBCLASS 00	GAU 215 2825	EXAMINER GANDHI
<b>**APPLICANTS:</b> Nakamoto Shigeru; Kondo Hideaki; Arima Juntaro;					
<b>**CONTINUING DATA VERIFIED:</b> <i>Wh</i>					
<b>** FOREIGN APPLICATIONS VERIFIED:</b> <i>Wh</i>					
PG-PUB		DO NOT PUBLISH <input type="checkbox"/>		RESCIND <input type="checkbox"/>	
Foreign priority claimed <input type="checkbox"/> yes <input checked="" type="checkbox"/> no 35 USC 119 conditions met <input type="checkbox"/> yes <input checked="" type="checkbox"/> no Verified and Acknowledged Examiners's initials				ATTORNEY DOCKET NO 500.41300X00	
TITLE : Semiconductor manufacturing apparatus and its diagnosis apparatus and operating system <small>U.S. DEPT. OF COMMERCE PAT. &amp; T.O. 430 (Rev. 1-2-92)</small>					

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<b>NOTICE OF ALLOWANCE MAILED</b>		<b>CLAIMS ALLOWED</b>	
		Total Claims _____ Print Claim for O.G. _____	
<b>ISSUE FEE</b>		<b>DRAWING</b>	
Amount Due	Date Paid	Sheets Drwg.	Figs. Drwg.
		Print Fig. _____	
<input type="checkbox"/> <b>TERMINAL DISCLAIMER</b>		Primary Examiner _____ Application Examiner _____	
		<b>PREPARED FOR ISSUE</b>	
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